



ANALOG AND MIXED MODE VLSI DESIGN

Vineeta P. Gejji

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The compact and well-illustrated text provides a thorough and clear understanding of analog and mixed mode VLSI design compatible with CMOS technology.

The book addresses the challenges in analog design and discusses the mixed signal layout issues. It describes the structure and properties of the MOS device and explains the derivation of its large signal and small signal models. It discusses the submicron CMOS fabrication process as well as the fabrication of capacitors and resistors and other building blocks of digital circuits. The design aspects of digital-to-analog and analog-to-digital data converters are elaborated thoroughly. Finally, the book discusses switched capacitor circuits which constitute a popular way of implementing analog signal processing circuits in standard CMOS technologies.

This book is suitable for undergraduate engineering students of Electronics, Electronics and Communication, Electronics and Instrumentation, Computer Science and Engineering, and Information Technology. In addition, this text will prove useful for professionals in the VLSI industry who want to brush up their fundamentals and get a quick reference to analog and mixed signal design.

KEY FEATURES :

- Contains a large number of solved examples.
- Offers chapter-end questions to probe a student's grasp of the subject matter.
- Includes appendices on "SPICE" and "Design of op-amp".

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